

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named
Inventor: Artur E. Balasinski et al.

Patent No.: 6,834,262 B1

Issue Date: December 21, 2004

Title: A SCHEME FOR IMPROVING THE
SIMULATION ACCURACY OF
INTEGRATED CIRCUIT PATTERNS
BY SIMULATION OF THE MASK

REQUEST FOR CERTIFICATE OF CORRECTION

ATTENTION: CERTIFICATE OF CORRECTIONS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are two originals of the form PTO-1050 correcting errors of a minor nature. A Certificate of Correction is requested under 35 U.S.C. § 255.

Please charge the \$100.00 fee to our American Express Card.

Respectfully submitted,



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Page 1 of 1

PATENT NO.: 6,834,262 B1

APPLICATION NO.: 09/608,158

DATED: June 30, 2000

INVENTOR(S): Artur E. Balasinski et al.

It is certified that error appears or errors appear in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Other Publications:

In the Balasinski, A et al. article, please delete "pproach" and insert --approach--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Evan Law Group LLC, 600 W. Jackson, Suite 625, Chicago, IL 60661

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